## Towards an Open Approach to Secure Cryptographic Implementations







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#### Transparency (as a measure of maturity)

• Block ciphers & symmetric encryption



#### Transparency (as a measure of maturity)

Secure cryptographic implementations



- 1. Side-channel (crypt)analysis: attacks taxonomy
- 2. Masking countermeasure: security vs. cost
- 3. Security definitions (authenticated encryption)
  - a. Nonce-respecting setting (i.e., AEL)
  - b. Nonce-misuse setting (i.e., AEmL)
- 4. Leakage-resistant AE designs (& implementations)
- 5. Conclusions (& the need of open evaluations)

#### Acknowledgments

- C. Archambeau
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- O. Bronchain
- G. Cassiers
- C. Dobraunig
- A. Duc
- F. Dupressoir
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- S. Duval
- S. Dziembowski
- S. Faust
- P.-A. Fouque
- B. Gierlichs C. Glowacz D. Goudarzi B. Grégoire V. Grosso S. Guilley T. Güneysu Chun Guo Qian Guo G. Herold A. Journault D. Kamel G. Leander L. Lerman G. Leurent
- I. Levi

- T. Malkin S. Mangard D. Masny C. Massart P. Méaux M. Medwed C. Momin A. Moradi M. Naya-Plasencia A. Olshevsky Y. Oren E. Oswald C. Paglialonga O. Pereira T Peters
- C. Petit

- K. Pietrzak
- R. Poussier
- E. Prouff
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- O. Reparaz
- M. Rivain
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- J. Schüth
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- N. Veyrat-Charvillon
- S. Vivek
- Weijia Wang
- C. Whitnall
- Yu Yu
- M. Yung

## Acknowledgments & cautionary note

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- Mixing (very) different abstraction levels
  - Hopefully in a consistent manner (*be forgiving if not*)

#### Outline

#### 1. Side-channel (crypt)analysis: attacks taxonomy

- 2. Masking countermeasure: security vs. cost
- 3. Security definitions (authenticated encryption)
  - a. Nonce-respecting setting (i.e., AEL)
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#### Leaking AES: $y = AES_K(x) \rightarrow L$





## Leakage function definition

- Leakages are vectors:  $\boldsymbol{L} = (L^1, L^2, ..., L^t)$ 
  - Made of many samples ( $t \approx 10^3 \cdot 10^6$ )



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- Leakages are noisy:  $L(x, K) \approx \delta(x, K) + N$ 
  - Signal-to-Noise Ratio:  $SNR^{i} = \frac{Var(\delta_{\chi}^{i})}{Var(N^{i})}$



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  - Signal-to-Noise Ratio:  $SNR^{i} = \frac{Var(\delta_{\chi}^{i})}{Var(N^{i})}$
- The shape of  $\delta \& N$  is technology-dependent
  - Their exact representation is unknown



## Basic facts (I)

- Computing less means leaking less
  - E.g., unprotected **32-bit** implem. (**HW** leakages)

# rounds	# ops.	# samples	MI (bits)	λ (bits)
	/ round	/ op.	/ sample	/ trace
10	100	5	$\frac{\log(32) = 5}{\left(1 + \frac{1}{\text{SNR}}\right)}$	$\frac{25,000}{\left(1+\frac{1}{\text{SNR}}\right)}$

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• Unprotected 128-bit implem. (HW leakages)

# rounds	# ops.	# samples	MI (bits)	λ (bits)
	/ round	/ op.	/ sample	/ trace
10	1	5	$\frac{\log(128) = 7}{\left(1 + \frac{1}{\text{SNR}}\right)}$	$\frac{350}{\left(1+\frac{1}{\mathrm{SNR}}\right)}$

#### Consequence (for theoretical analysis)

 Games that give the adversary the ability to compare the leakages of two identical device states are in general trivial to win. For example, given a keyed offline leakage oracle L(.,K):

$$\Pr\left[A_{\mathrm{SC}}^{\boldsymbol{L}(.,K)}(x_0, x_1, \boldsymbol{L}(x_b, K)) = b | K, b \leftarrow \$\right] \approx 1$$

- Just compare  $L(x_b, K)$  with  $L(x_0, K)$  and  $L(x_1, K)$
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⇒ Distinguishing games without anything fresh and secret in the challenge are trivial to win

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 Key recovery attacks may not easily exploit all leakage samples (since A needs to guess the state), leading to reduced « effective » λ's, e.g.,

exploited	# ops.	# samples	MI (bits)	eff. λ (bits)
# rounds	/ round	/ op.	/ sample	/ trace & subkey
1	1	pprox 2 (indep.)	$\frac{\log(128) = 7}{100}$	$\frac{14}{100}$

• One key byte recovered in  $\approx \frac{128}{0.14} \approx 1000$  traces

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- With the masking countermeasures (see next)
- (128-bit example, 32-bit case significantly harder)

## Basic facts (III)

- (q, r)-bounded SCAs are « continuous » attacks
  - with q different message blocks per key
  - and each measurement repeated r times
- ⇒ Typical success probability (e.g., for key recovery):

$$\Pr\left[A_{\mathrm{KR}}\left(x_1, \boldsymbol{L}(x_1, K), \dots, x_q, \boldsymbol{L}(x_q, K)\right) \to K | K \leftarrow \$\right] \approx 2^{-128 + q \cdot \lambda(r)}$$

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- There are two main types of attacks (jargon)
  - SPA: q is a small constant (e.g., thanks to re-keying)
  - DPA: q can be large & is adversarially chosen
- Larger r's can improve the SNR (average the noise)

- Key Recovery (KR) attacks (with known/chosen  $x_i$ 's)  $\Pr\left[A_{\mathrm{KR}}\left(x_1, \boldsymbol{L}(x_1, K), \dots, x_q, \boldsymbol{L}(x_q, K)\right) \rightarrow K | K \leftarrow \$\right] \approx 2^{-128+q \cdot \lambda(r)}$ 
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  - May require large amounts of leakage vectors to succeed
    Or have bounded success probability in case of SPA
- Message Comparison (MC) attacks (with fresh challenge)  $\Pr\left[A_{MC}^{L(.,.)}(x_0, x_1, L(x_b, K)) = b | K, b \leftarrow \$\right] \approx 2^{-128 + D\left(L(x_0, K); L(x_1, K)\right)}$ 
  - Significanly simpler than KR but not trivial for all  $x_0, x_1$  (!)
  - Depends on similarity of the message blocks' leakages
- State Comparison (SC) attacks (with keyed oracle)

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#### Noise (hardware) is not enough





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- Additive noise ≈ cost × 2 ⇒ security × 2
   ⇒ not a good (crypto) security parameter
- $\approx$  same holds for all hardware countermeasures

• Example: Boolean encoding

$$y = y_1 \oplus y_2 \oplus \cdots \oplus y_{d-1} \oplus y_d$$

• With  $y_1, y_2, \dots, y_{d-2}, y_{d-1} \leftarrow \{0, 1\}^n$ 

• Private circuits / probing security [ISW03]



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• d-1 probes do not reveal anything on y

• Private circuits / probing security [ISW03]



• But *d* probes completely reveal *y* 

#### Masking (concrete view)

• Private circuits / probing security [ISW03]



serial implementation.

• Noisy leakage security [PR13]

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• Bounded information  $MI(Y; L) < MI(Y_i; L_{Y_i})^d$
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$a_1b_1$	$a_{1}b_{2}$	$a_1b_3$
$a_{2}b_{1}$	$a_{2}b_{2}$	$a_{2}b_{3}$
$a_{3}b_{1}$	$a_{3}b_{2}$	$a_3b_3$

partial products

- Linear operations:  $f(a) = f(a_1) \oplus f(a_2) \oplus \cdots \oplus f(a_d)$
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$$\begin{bmatrix} a_1b_1 & a_1b_2 & a_1b_3 \\ a_2b_1 & a_2b_2 & a_2b_3 \\ a_3b_1 & a_3b_2 & a_3b_3 \end{bmatrix} + \begin{bmatrix} 0 & r_1 & r_2 \\ -r_1 & 0 & r_3 \\ -r_2 & -r_3 & 0 \end{bmatrix}$$

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compression

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partial products
$$refreshing \quad compression$$

$$a_1b_1 \bigoplus a_1b_2 \bigoplus a_1b_3 = a_1b \text{ leaks on } b$$

⇒ Quadratic overheads & randomness

(Many published optimizations [R+15,Be+16,GM18])

#### Statistical intuition (2 shares)



• Leakage mean vector for  $Y = 0.1 = [0.5 \ 0.5]$ 

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• Leakage mean value for Y = 0, 1 = 1

#### Case study: ARM Cortex M4 [JS17]



#### 2.7

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number of shares

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- SPA security expected to be (much) cheaper

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### Authenticated Encryption (AEAD)

• Why not extending [RS06]'s all in one definition?

2.1



• A cannot ask a decryption query on (N, AD, C) after C is returned by an (N, AD, .) encryption query

## Authenticated Encryption (AEAD)

• Why not extending [RS06]'s all in one definition?



- A cannot ask a decryption query on (N, AD, C) after C is returned by an (N, AD,.) encryption query
- Problem: the leakage of ideal objects (which do not have implementations) seems difficult to define

#### **Ciphertext Integrity**



#### Ciphertext Integrity with Leakage



• CIL1: leagage in encryption only [Be+18]

### Ciphertext Integrity with Leakage



- CIL2: leagage in encryption and decryption [BPPS17]
- Natural extensions (no definitional challenges) with many applications (e.g., secure bootloading)

### **Chosen Ciphertext Security**



#### CCA Security with Leakage [GPPS18]



• CCAL1: leakage in encryption

#### CCA Security with Leakage [GPPS18]



CCAL2: leakage in encryption and decryption

#### CCA Security with Leakage [GPPS18]



+ challenge Ldec\* (applications: IP protection, ...)

## The challenge leakage controversy (I)

• [MR04] (and [NS09,BG10,...]): indistinguishability with Lenc\* is hard (one bit breaks it with p = 1)

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- So it is quite tempting to ignore it
- Which can make sense (e.g., if you tolerate « local attacks » but not « global » security degradations)
  - Leakage-resilience vs. leakage-resistance

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     Leakage-resilience vs. leakage-resistance
- Ignoring challenge leakages means that an implementation leaking messages in full is OK
  - This is not what we want in general / theory
  - It can have big impact (e.g., TLS [CHV03], [AP13], ...)
    - Different attacks but they show plaintext leakage matters

## The challenge leakage controversy (II)

- If we do not make it part of the definition it will never be a goal for cryptographers & engineers
  - Cryptographers: minimize the message manipulation

3.5

• Engineers: minimize message leakage, e.g., with special encodings (which is not much studied yet)

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  - Even if results are not ideal (e.g., no negl. Adv.)

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- We need to understand what can be achieved
  Even if results are not ideal (e.g., no negl. Adv.)
- Technically: more greyscale view than [MR04]
  - Challenge leakages allow Message Comparison (MC) attacks which are not always tivial, e.g.,
    - Remote timing attacks: scalar leakages (vs. vectors)
    - Proxy re-encryption: messages are not chosen

#### An motivating example

• Tree-based leakage-resilient PRF [GGM84, FPS12]



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• Tree-based leakage-resilient PRF [GGM84, FPS12]



- Leads to simple MC attacks
  - Message encrypted bit per bit ⇒ no algorithmic noise
  - Constant block cipher inputs « all zeros » and « all ones » easy to distinguish with HWs [B12]



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#### Misuse-Resistance (MR) [RS06]



- Black box: only identical (*N*, *M*) pairs should be at risk
- Typically achieved by having a 2-pass mode (e.g., SIV)
#### Misuse-Resistance (MR) [RS06]



• With leakage: a SC attack against  $M_1 = \{x_1, x_2, x_3, x_4\}$  and  $M_2 = \{x_1, x_2, x_3, x_4^*\}$  leaks that they first blocks are equal

#### Misuse-Resilience (mR) [ADL17]



- Fresh challenge nonce circumvent this impossibility
  - Intuition: leaves mostly MC attacks and DPAs

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- $\approx$  a choice between the need for applications to limit the leakage or for implementers to control nonces
- Strongest (*leak.-resist*.) def.: AEML=CIML2+CCAmL2+MR

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   a. Misuse-resilience/leakage-resistance: CCAmL [GPPS18]
   b. Misuse-resistance/leakage-resilience: CCAMI [BMOS17]
- $\approx$  a choice between the need for applications to limit the leakage or for implementers to control nonces
- Strongest (*leak.-resist*.) def.: AEML=CIML2+CCAmL2+MR
- Weaker variants can be meaningful: for instance AEmL=CIML2+CCAmL2 [Be+19], CPAI1 [DM19], ...

1. Side-channel (crypt)analysis: attacks taxonomy

- 2. Masking countermeasure: security vs. cost
- 3. Security definitions (authenticated encryption)a. Nonce-respecting setting (i.e., AEL)
  - b. Nonce-misuse setting (i.e., AEmL)
- 4. Leakage-resistant AE designs (& implementations)
- 5. Conclusions (& the need of open evaluations)

#### Seed: a leakage-resilient MAC



- Forgeries can exploit two attack paths
  - a DPA against the long-term key K
  - a DPA against the tag verification  $\tau = \tau_c$ ?
    - By monitoring the comparison with random tags
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- Leakage on the dotted parts can be unbounded
- Can we « minimize the attack » surface?

#### First tweak: LR tag verification



- Natural option: inverse-based tag verification
  - Only performs comparisons with a public *h*
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- How to generalize this to an AE scheme?

# An AE scheme satisfying CIML2

• First ignoring confidentiality with leakage



- Many parts of the design can leak in full
  - Strong motivation for composite definitions: allow using the weakest possible assumptions for integrity and confidentiality (which are not the same)

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- How to add confidentiality guarantees?

# Engineering approach to CCAL security



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- Requires protecting all the BC blocks against DPA
  - And to deal with the (unavoidable) MC attacks
- Typically leads to (very) expensive implementations

#### A CCAmL2 encryption scheme



 Most BC executions can be protected against SPA only (+ two DPA-secure BC calls and security against MC attacks)





- Formally, modeled as leak-free
- Graceful degradation seems possible



4.6

# Security reductions (simplified)



4.6

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4.6

• S1P: <u>1-pass</u> (online), CIML2, CCAmL1 [GPPS19]



- Encourages « leveled implementations »
  - Strongly protected TBC: high-order masking
  - Weakly protected permutation: low-latency
- For such implementations, two different primitives are not an issue (since implementations are different)

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- For such implementations, two different primitives are not an issue (since implementations are different)
- (+ beyond birthday w.r.t. TBC key, multi-user security)

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• Performance gains of leveled implementations



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# A theory to guide practice?

- Overall,  $\exists$  a wide zoo of definitions including
  - Leakage-resilience vs. leakage-resistance
  - Misuse-resilience vs. misuse-resistance
  - Leakage in encryption and decryption
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    - What is best in practice still has to be evaluated

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⇒ Hope: strong assumptions in the proofs/analyzes indicate where implementers must put most efforts

• We have good ingredients ⇒ how to mix them?

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- Classification of existing AE schemes
  - E.g., NIST lightweight competition candidates
- Links between the different security notions
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- Anything leading to simple(r) hardware guidelines...

#### standard practice

# evidence-based evaluations (assumptions tested per device!)



# **Evaluation challenge**





evidence-based evaluations (assumptions tested per device!)



# **Evaluation challenge**


# **THANKS** http://perso.uclouvain.be/fstandae/

#### SUPPLEMENTARY SLIDES

#### Scalability & composability





*t*-probing security [ISW03] any *t*-tuple of shares in the protected circuit is independent of any sensitive variable

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#### Scalability & composability



```
q_1 + q_2 \le q
```

 $q_1$  internal probes

 $q_2$  output probes

Problem: the cost of testing probing security increases (very) fast with circuit size and the # of shares (∃ many tuples) [Ba+15]

*q*-(Strong) Non Interference [Ba+16]: a circuit gadget (e.g., f1) is (Strongly) Non-Interferent if any set of  $q_1 + q_2$  probes can be simulated with at most  $q_1 + q_2$  (only  $q_1$ ) shares of each input

 $D(\text{input shares}||\text{probes}) \approx D(\text{input shares}||\text{simulation})$ 

#### Separation result (simplified)

• Why CI+CCA (while in black box: CI+CPA = PI+CCA)?

**B.1** 

- Why CI+CCA (while in black box: CI+CPA = PI+CCA)?
- Let AEAD be CIML2 & CCAmL2 with *Lenc* and *Ldec* 
  - Define AEAD' such that
    - Lenc'(K, M) = Lenc(K, M) + Lenc(K', M')
    - Ldec'(K, C) = Ldec(K, C) + Lenc(K', M')
  - AEAD' is still CIML2 but not CCAmL2 anymore
  - Attack: use the *Ldec*' query to leak about M' and then use M' as challenge plaintext

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- Attack: use the *Ldec*' query to leak about M' and then use M' as challenge plaintext
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• (Second apparition of a recuring issue: somewhat artificial attack related to the difficulty to model *L*)

- C.1
- Analyses must deal with the **information** and the **computation**al complexity of the leakage function
  - Information: obvious (full leakage  $\Rightarrow$  no secrecy)
  - Computation: avoid « precomputation attacks » [DP08]
- Background: the shape of *L* is unknown
  - We don't even know its complexity class, e.g.,
    - Solving Maxwell's equations for an AES circuit takes days
    - But a physical circuit provides an instantaneous answer



• Information restriction

HILL pseudoentropy [DP08]

seed-preserving PRG [YPSM10] (≈ hard to invert leakages [DKL09])



. .

• Information restriction + computation restriction

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only computation leaks [MR04]

+ alternating structure [DP08]

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oracle-free leakage function [YPMS10]







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- simulatability [SPY13]
  - Ø



oracle-free leakage function [YPMS10]







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- Information restriction + computation restriction
- Safer strategy: try proofs with both combinations
  - Weaker physical assumption but idealized analysis
  - Stronger physical assmption in the standard model





#### Simulatable leakage (I)

- **L** is hard to model  $\Rightarrow$  just don't model it
- Give public I/O access to device & setup



## Simulatable leakage (I)

- L is hard to model  $\Rightarrow$  just don't model it
- Give public I/O access to device & setup



- Assume L(x, K) can be simulated
  - Using the same hardware as the target
  - But without knowing the secret key *K*

#### Simulatable leakages (II)

- L(x, K) can be simulated without knowledge of K
  - E.g., FPGA implementation, 128-bit architecture



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- *L*(*x*, *K*) can be simulated without knowledge of *K* 
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- Simulated traces should be consistent with x and y
- Simple proposal [SPY13]: « split & concatenate »  $S(x, K, y) = L^{1/2}(x, K^*, y^*) ||L^{2/2}(x^*, K^*, y)$

#### The Longo et al. distinguisher [Lo+14]

• Intra-trace correlation:  $\rho(L^{t_i}, L^{1:t_{2500}})$ , real traces



2.5

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• Intra-trace correlation:  $\rho(L^{t_i}, L^{1:t_{2500}})$ , real traces



• Same, with simulated traces  $L^{1/2}(x, K^*, y^*) ||L^{2/2}(x^*, K^*, y)|$ 



Fixing this requires modeling the physics of *L* (but the goal of simulatability was to avoid such modeling)

#### Another (new) approach

• Just look (exhaustively) for a key  $K^*$  such that  $BC_{K^*}(x) = \tilde{y} \rightarrow l_{\tilde{y}}$  and  $e = |l_y - l_{\tilde{y}}|$  is small



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• Add key-dep. noise s.t.  $\forall x, K, K^*$ ,  $E(l_{\Delta}) \neq E(l_{\Delta^*})$ 

#### Statistical distinguisher

- Assume a perfect (additive) leakage model
- Remove the contribution l<sub>y</sub> for each x, K pair



simulated distributions



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simulated distributions



- The difference btw. variances decreases in  $e^2$
- Error *e* dercreases linearly in sim. complexity *C* 
  - Depending on the leakage function (experiments needed)

#### Distinguishing complexity

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• (Imperfect model:  $l_{\Delta} + e'$  and  $l_{\Delta^*} + e + e'$  get closer) real simulation

#### Simulatability vs. pseudoentropy



- [DP08] require  $H^{HILL}(K|L) > k \lambda$
- Roughly:  $\forall l, \exists a \text{ set of } 2^{k-\lambda} \text{ keys s.t. } BC_{K^*}(x) \rightarrow \tilde{l} \stackrel{c}{\approx} l$

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- (Without the comp. limit., SPA samples become SC samples)
### Simulatability vs. pseudoentropy



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### Simulatability vs. pseudoentropy

computational limits do not help

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- (Different parts of the leakages raise different challenges)
- $\Rightarrow$  a fraction  $\frac{1}{2^{\lambda}}$  of the keys can be used to simulate
- Theory [FH15]: *q*-SIM and H<sup>HILL</sup> disconnected
- Cryptanalysis: q-SIM  $\leq H^{HILL} \ll CCAmL$ (for 1 block)

## Simulatable leakage definition

- C.10
- More formally, ( , ) has q-simulatable
  leakages of ∃ a simulator S<sup>L(.,.)</sup> such that the bit b
  in the following game is hard to guess

Game $q$ -sim( $A$ , $\clubsuit$ , $S^{L(.,.)}$ , $b$ ) with $K$ , $K^*$ uniformly random		
q queries	response if $b = 0$	response if $b = 1$
enc(x)	$y = \mathrm{BC}_k(x),  \boldsymbol{L}(x, K)$	$y = \mathrm{BC}_k(x), S^{L(.,.)}(x, \mathbf{K}^*, y)$
1 query	response if $b = 0$	response if $b = 1$
gen(z,x)	$S^{\boldsymbol{L}(.,.)}(\mathbf{z},\boldsymbol{x},\boldsymbol{K})$	$S^{L(.,.)}(z, x, K^*)$

(Not exactly real vs. simulated due to the gen query)







 $\rightarrow$  **L** (x, K<sub>0</sub>, K<sub>1</sub>) || **L** (x, K<sub>1</sub>, K<sub>2</sub>)

 $\rightarrow$  **S**  $(x, K_0^*, K_1) \mid \mid L(x, K_1, K_2)$ 



 $\rightarrow$  **L**  $(x, K_0, K_1) \mid \mid$  **L**  $(x, K_1, K_2)$ 

 $\rightarrow$  **s** (x, K\_0^\*, K\_1) || **L** (x, K\_1, K\_2)

- *K*<sub>2</sub>

 $\rightarrow$  **s**  $(x, K_0^*, K_1^*) \mid \mid$  **L**  $(x, K_1, K_2)$ 



 $\rightarrow$  **L** (x, K<sub>0</sub>, K<sub>1</sub>) || **L** (x, K<sub>1</sub>, K<sub>2</sub>)

 $\rightarrow$  **s** (x, K<sub>0</sub><sup>\*</sup>, K<sub>1</sub>) || **L** (x, K<sub>1</sub>, K<sub>2</sub>)

 $\rightarrow$  **s**  $(x, K_0^*, K_1^*) \mid \mid$  **L**  $(x, K_1, K_2)$ 

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